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APPLICANTS
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 This application is a 371 of PCT/JP04/16174 10/25/2004

**** FOREIGN APPLICATIONS *******
 JAPAN 2003-373514 10/31/2003

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Foreign Priority claimed 35 USC 119(a-d) conditions met	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No	<input type="checkbox"/> Met after Allowance	STATE OR COUNTRY	SHEETS DRAWINGS	TOTAL CLAIMS	INDEPENDENT CLAIMS
Verified and Acknowledged	/MAGID Y DIMYAN/ Examiner's Signature	Initials	JAPAN	8	8	3

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TITLE
 Semiconductor integrated circuit and design method thereof

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